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	1115051145101151			Application Number	Not assigned yet
INFORMATION DISCLOSURE				Filing Date	On even date herewith
	STATEMENT BY APPLICANT			First Named Inventor	K, YAMASHITA, et al.
				Art Unit	Not assigned yet
(use as many sheets as necessary)			<u>)                                    </u>	Examiner Name	Not assigned yet
Sheet	1	of	1	Attorney Docket Number	501.41072CX1

			U.S. PATENT	DOCUMENTS	
Examiner Initials'	Cite No. <sup>1</sup>	Document Number  Number-Kind Code <sup>2</sup> (if known)	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
NV		5,724,004	03-1998	Reif et al.	Tigutes Appeal
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		FOREIC	N PATENT DO	CUMENTS		
Examiner	Cite	Foreign Patent Document	Publication Date		Pages, Columns, Lines,	
Initials'	No.1	Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (If known)	MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	₽*

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.1	ite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the			
NV		"Thermally stable power HBT by the bias circuit using Schottky diode" Keiichi Murayama et al., Semiconductor Device Research Center, Matsushita Electronics Corporation, pp. 1-4 and p.65			
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